

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	motherboard and receivers and controllers and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 10:17
L2	252	motherboard and receivers and controllers and ((zero adj ohm adj resistor) or jumper)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 10:58
L3	2	361/760-764.ccls. and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:00
L4	8	361/760-803.ccls. and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:01
L5	10	"327"/\$.ccls. and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:04
L6	79	(motherboard or (circuit adj board)) and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:04
L7	29	(motherboard or (circuit adj board)) with (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:18
L8	22	(configurable adj circuit adj board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:20
L9	22	(configurable adj circuit adj board)" .clm"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:20

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L7	29	(motherboard or (circuit adj board)) with (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 11:04